

Section 1 #3

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Toshinori NAKAYAMA

Application No.: 09/626,146

Filed: July 26, 2000



ATTN: Application Branch

Docket No.: 106364

For: SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

NOTICE TO FILE MISSING PARTS STATUS REQUEST

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

The above-captioned patent application was filed on July 26, 2000. A copy of the postcard receipt is attached hereto.

The Notice to File Missing Parts has not yet been received. It is respectfully requested that the Notice to File Missing Parts be immediately forwarded to the attorneys of record at the address set forth below.

If there are any questions regarding this matter, please contact the undersigned at the telephone number set forth below.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

Joel S. Armstrong
Registration No. 36,430

JAO:JSA/cmm

Date: October 26, 2000

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

RECEIPT FOR FILING OF PAPERS

The following papers have been filed:

Trans. Ltr. & Ck 110592 \$912, 37 pp. spec., 28 claims, Abst., 8 sheets dwgs (1-14), Claim for Priority & cert. copies of JP 11-213184 07/28/1999, JP 2000-173294 06/09/2000

Name of Applicant: Toshinori NAKAYAMA

Serial No.: New U.S. Patent Application

Atty. File No.: 106364

Title (New Cases): SUBSTRATE FOR SEMICONDUCTOR DEVICE,
SEMICONDUCTOR CHIP MOUNTING SUBSTRATE,
SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION
THEREOF, AND CIRCUIT BOARD, TOGETHER WITH
ELECTRONIC EQUIPMENT

Sender's Initials: JAO/cmm

57/2



PATENT OFFICE DATE STAMP

**COPY TO BE STAMPED BY PATENT OFFICE
AND RETURNED BY MESSENGER**

6